ACROPOLIS TECHNICAL CAMPUS

DEPARTMENT OF ELECTRONICS & COMMUNICATION.

MID SEMESTER TEST-1 (September-2017) Branch/Year/Semester/Sec: EC/3nd Yr/5th Sem/A & B Sub Name & Code: Microprocessor & Microcontroller (EC5003)

Max Marks: 20

Max Time: 1Hr.

Part (A)

Sol	ve the Following:	(05)	
(A (E	eady pin of a microprocessor is used A) to indicate that the microprocessor is ready to receive inputs. B) to indicate that the microprocessor is ready to receive outputs. C) to introduce wait states. (D) to provide direct memory access	ss.	
(A (E (C	Chese are two ways in which a microprocessor can come out of Halt state. A) When hold line is a logical 1. B) When interrupt occurs and the interrupt system has been enabled. C) When both (A) and (B) are true. D) When either (A) or (B) are true.		
(A	D RAM refers to A) Synchronous DRAM (B) Static DRAM (C) Semi DRAM (D) Second DRAM		
(A (E (C	Thich of the following is true with respect to EEPROM? A) contents can be erased byte wise only. B) contents of full memory can be erased together. C) contents can be erased using ultra violet rays D) contents can not be erased		
(1	Which microprocessor pins are used to request and acknowledge a DMA transfer (A) reset and ready (B) ready and wait (C) HOLD and HLDA (D) None o these PART-B (Attempt any three)	т?	
Que.1	Explain the Microprocessor Operations.[CO.3]	(05)	
Que. 2 Que. 3	1. INTR,INTA, 2.HOLD,HLDA 3. READY [CO.3,6] (05)		
)ue. 4	Draw the Architecture diagram of Intel's 8086 & explain the following:		
	Instruction Fetch Queue, Flag Register [CO.3]	(05)	
)ue. 5	What is Interrupt? Explain the following:		
	1) Hardware Interrupt & Software Interrupt		
	2) Maskable & Non- Maskable Interrupt. [CO.3,6]	(05)	
ue.6	Explain the term BIOS . [CO.1]	(05)	

Shivajirao Kadam hashluta of Technology & Management - Technical Campus, INDORE



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Sub Name & Code: Microprocessor & Microcontroller (EC5003)

Max Marks: 20

Max Time: 1Hr.

Part (A)

Solve the Following:	(05)
Q.1 which of the Following holds the ROM, CPU, RAM and expansion cards a) Hard disk b) Floppy disk c) Mother board d) none of the a	s? bove
Q.2 The language that the computer can understand and execute is called a) Machine language b) Application software c) System program d) none of the above	
Q.3 Actual execution of instructions in a computer takes place in a) ALU b) Control Unit c) Storage unit d) None of the above	e
Q.4 Execution of two or more programs by a single CPU is known as: a) Multiprocessing b) Time sharing c) Multiprogramming d) None of the above	
Q.5 Operating system a) Link a program with the subroutines it references b) Provides a layered, user-friendly interface c) Enables a programmer to draw a flowchart d) None of the ab	pove
PART-B (Attempt any three)	
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Que. 1 Draw the Architecture diagram of Intel's 8255 & explain the term: a. Data bus buffer b. Read/Write Control Logic [CO.1] Que. 2 Explain the Hardware controlled data transfer in terms of DMA Control.	roller. (05)
Que. 1 Draw the Architecture diagram of Intel's 8255 & explain the term: a. Data bus buffer b. Read/Write Control Logic [CO.1] Que. 2 Explain the Hardware controlled data transfer in terms of DMA Control [CO.6] Que. 3 Draw the Control Word Format of Intel's 8255 & explain BSR Mode	roller. (05)
Que. 1 Draw the Architecture diagram of Intel's 8255 & explain the term: a. Data bus buffer b. Read/Write Control Logic [CO.1] Que. 2 Explain the Hardware controlled data transfer in terms of DMA Control [CO.6] Que. 3 Draw the Control Word Format of Intel's 8255 & explain BSR Mode With an Example. [CO.5]	roller. (05)
 Que. 1 Draw the Architecture diagram of Intel's 8255 & explain the term: a. Data bus buffer b. Read/Write Control Logic [CO.1] Que. 2 Explain the Hardware controlled data transfer in terms of DMA Control [CO.6] Que. 3 Draw the Control Word Format of Intel's 8255 & explain BSR Mode With an Example. [CO.5] Que. 4 Draw the Architecture diagram of Intel's 8257 & explain the term: 	roller. (05) (05)
 Que. 1 Draw the Architecture diagram of Intel's 8255 & explain the term: a. Data bus buffer b. Read/Write Control Logic [CO.1] Que. 2 Explain the Hardware controlled data transfer in terms of DMA Contrologic [CO.6] Que. 3 Draw the Control Word Format of Intel's 8255 & explain BSR Mode With an Example. [CO.5] Que. 4 Draw the Architecture diagram of Intel's 8257 & explain the term: 1. Priority resolver 2. DMA Address Register [CO.6] 	(05) (05) (05)

Shivajirao Kadam Indiante of Traimplogy & Management - Technical Cambus, INDORE

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Branch/Year/Semester/Sec: EC/2nd Yr/3th Sem/ MID SEMESTER TEST-2 (November-2017) Sub Name & Code: Measurement & Instrumentation (EC3005)

Max Marks: 20

Max Time: 1Hr.

ran (A)				
Solve the Following:	(05)			
 The use of instruments is merely confined with Instruments. (a) Absolute (b) indicating (c) recording Which of the following instruments indicate the instance measured at the time at which it is being measured? 	(d) integrating ntaneous value of the electrical quantity being (b) Indicating instruments			
time. (a) Absolute (b) Indicating (c) Recording 4. Which of the following are integrating instruments?	(1) Intermeting			
PART-B (Attempt any three)				
Que. 1 Why Maxwell's bridge is used?				
Que. 2 What are the Advantages & disadvantages of Ma	axwell Bridge?			
One 3 Draw the Hay's Bridge & Derive the expression	for the unknown quantity.			

Que. 4 Explain the advantages & disadvantages of Hay's bridge.

Que. 5 Draw L.V.D.T & explain in detail.

Explain the Advantages & Disadvantages of Strain gauge. Que.6

Shivajirao Kadam Ins Management - Technical Campus, INDORE